



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

SCHAUER ET AL 1 PCT

SERIAL NO.:

09/890,656

EXAMINER: THIEN F. TRAN

FILED:

AUGUST 2, 2001

GROUP: 2811

TITLE:

SEMICONDUCTOR WAFER COMPRISING A THIN EPITAXIAL

SILICON LAYER AND METHOD FOR PRODUCING SAME

HJONES

RESPONSE TO RESTRICTION REQUIREMENT

ATTN: BOX NON FEE AMENDMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Dear Sir:

In response to the Office Action dated OCTOBER 1, 2002, Applicant respectfully responds as follows:

REMARKS

The Patent Examiner has required a restriction to one of the following two (2) inventions:

Group I: Claims 13-15, drawn to a semiconductor device, classified in class 257, subclass 56.